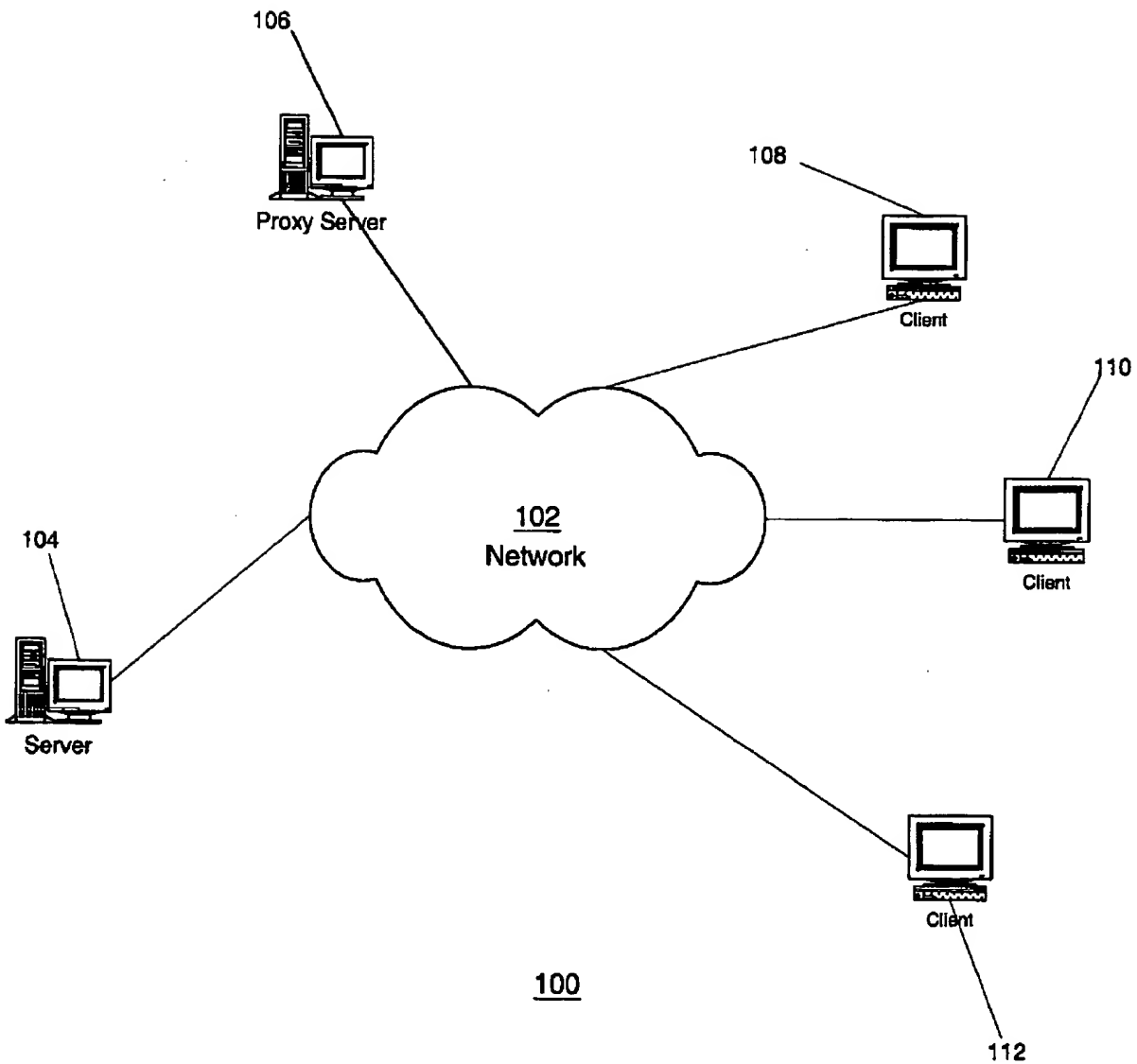


Figure 1

AUS9-2000-0403-US1



The diagram illustrates a computer system architecture. At the top, two processors, **Processor 202** and **Processor 204**, are connected to a horizontal **System Bus 206**. Below the system bus is a block containing a **Memory Controller/Cache 208** and an **I/O Bridge 210**. **Local Memory 209** is connected to the memory controller/cache. A vertical **I/O Bus 212** extends from the I/O bridge. On the left side of the I/O bus, a **Graphics Adapter 230** and a **Hard Disk 232** are connected. On the right side, three **PCI Bus Bridge** units (**214**, **222**, and **224**) are connected. Each bridge is connected to a horizontal **PCI Bus** (labeled **216**, **226**, and **228** respectively). The bridge **214** is also connected to a **Modem 218** and a **Network Adapter 220**. The entire system is labeled **200**.

AUS9-2000-0403-US1

001E80" 542E9960

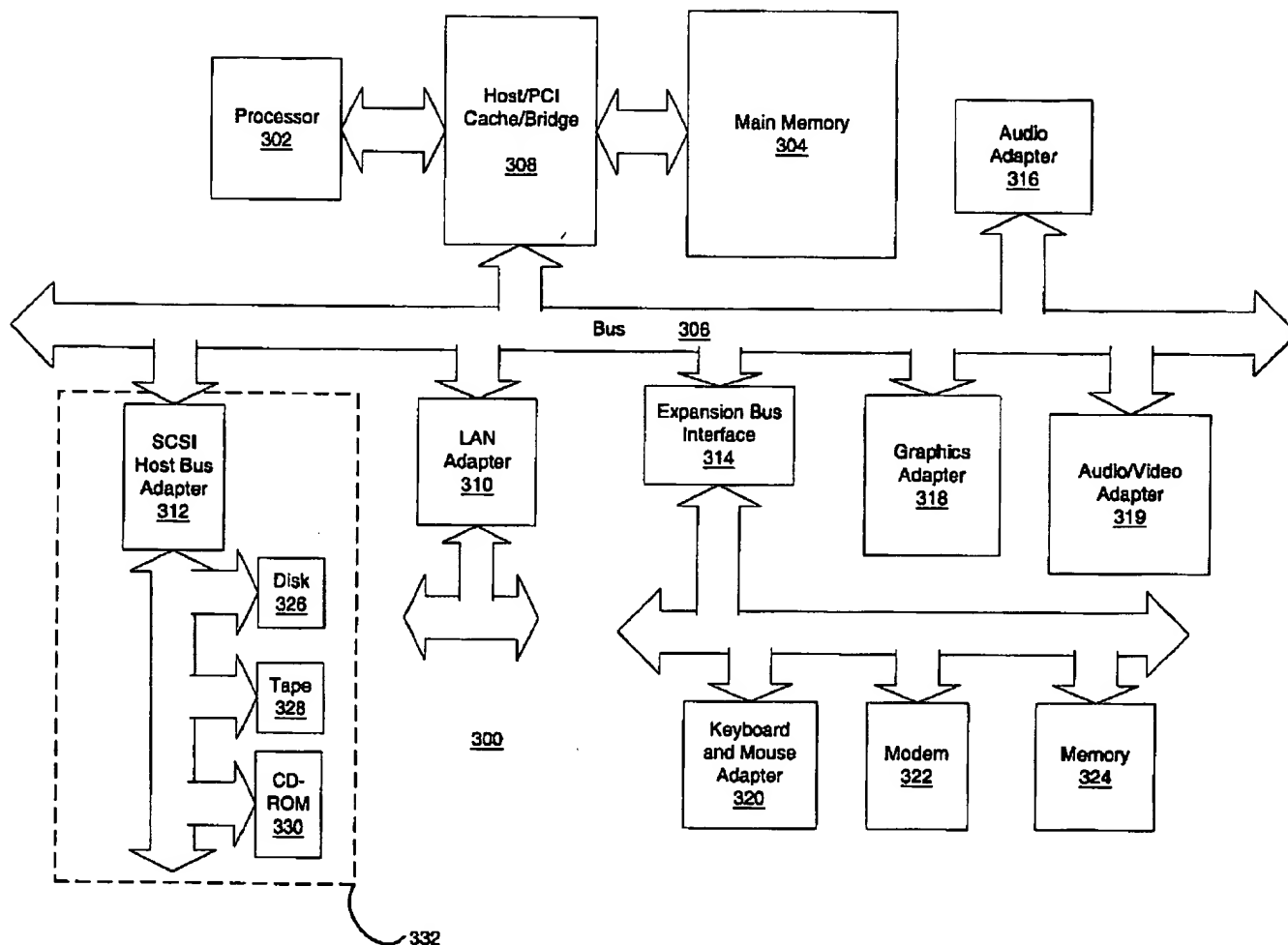


Figure 3

AUS9-2000-0403-US1

AUS9-2000-0403-US1

AUS9-2000-0403-US1

400

[illegible]

AUS9-2000-0403-US1

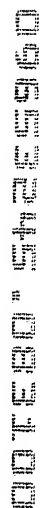
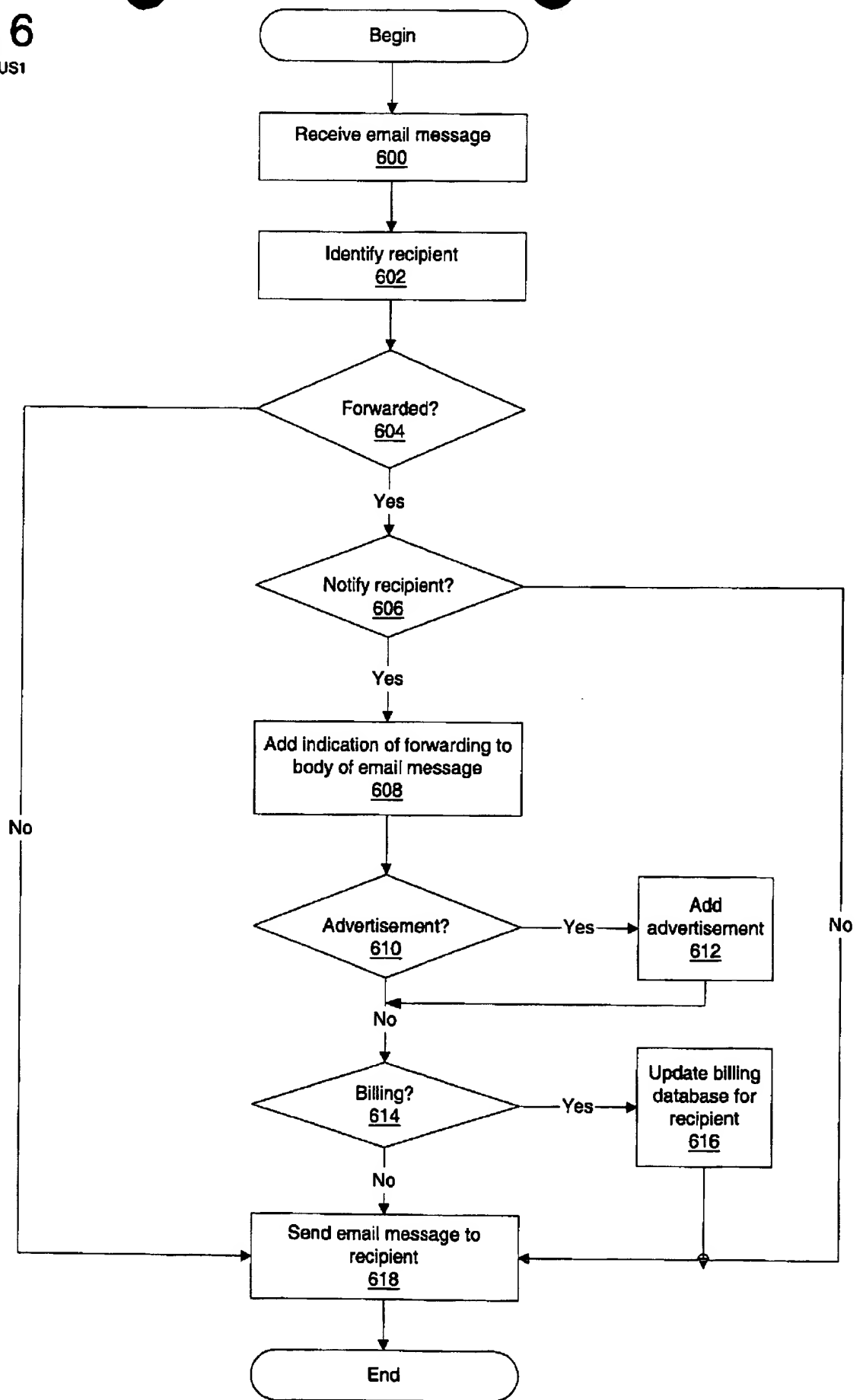


Figure 6
AUS9-2000-0403-US1



007580" 5425960